

Government Polytechnic, Jatwada, Jansath Muzaffarnagar

NAME OF Faculty/Year		Electronics Engg. V Sem				ROOM No.	Session 2024-25 w.e.f. 12.08.2024			
TIME DAY	I	II	III	IV	01:20-01:40	V	VI	VII	VIII	
	10:00-10:50	10:50-11:40	11:40-12:30	12:30-01:20		01:40-02:30	02:30-03:20	03:20-04:10	04:10-05:00	
MON	OFC(L) CR 215	AL	MP (L) CR - 215	DSS	RECESS	Prog in C (LAB) CR - 215 NS				
TUE	IMED (L) CR 215	JK	MP (L) CR - 215	DSS		CR 215 NS	OFC(L) CR 215 AL			
WED	CE (L) CR 215	SC	IMED (L) CR 215	JK		MP (LAB) L - 221 SC				
THUR	Prog in C (L) CR 215	NS	OFC(L) CR 215	AL		CE (L) CR 214	SC	SCA (L) CR 215 DSS	SCA (L) CR 215 SK	
FRI	CE (LAB) L - 202			SC		MPW (LAB) L - 202			SCA (L) CR 215 SC	
SAT	MPW (LAB) L - 202		DSS/AV	IMED (L) CR 215 JK		OFC (LAB) L - 221 AL				

DSS	Dr.Sonveer Singh
JK	Mr Janmejai Kumar
AV	Mr Akash Vajpai
AL	Mr. Ashish Lamba
SC	Mr.Subhash Chand

IMED	Industrial Management & Entrepreneurship Development
M & PD	Microprocessor & Peripheral Device
OFE	Optical Fiber Communication
CE	Consumer Electronics
Prog in C	Programming in C
MPW	Minor Project Work
SCA	Student Center Activity

(Signature)
शैक्षिक अधिकारी

(Signature)
प्रधानाचार्य
राजकीय पालीटेक्निक
जानसठ (मुजफ्फरनगर)