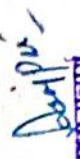



**Government Polytechnic, Jatwada, Jansath Muzaffarnagar**

NAME OF Faculty/Year		Electronics Engg. III-Sem				ROOM No.	w.e.f. 12.08.2024	
I YEAR	II	III	IV	V				
TIME DAY	10:00-10:50	10:50-11:40	11:40-12:30	12:30-01:20	01:40-02:30			
MON	BCR-113 AM-III(LEC)	DMK	BCR-113 DE(LEC)	SK	EDC(LAB)			
TUE	BCR 113 EE-II(LEC)	SG	BCR-113 EDC(LEC)	AL	AM-III(LEC)			
WED	BCR-113 EDC(LEC)	AL	BCR-113 ES(LEC)	SK	DMK CR-215			
THUR	AM-III(LEC) BCR-113 DMK	L-207	EW(LAB)	SG	DE(LAB)			
FRI	BCR-113 DE(LEC)	SK	BCR-113 EDC(LEC)	AL	SCA CC-203 SK L-217			
SAT	L-207	EW(LAB)	SK	ES(LEC) BCR-113	CC-203 EE-II(LEC) SG L-221			
					L-217 EE-II(LAB) SG			

DMK	DR. MOHIT KUMAR
AL	MR. ASHISH LAMBA
SK	MR. SOHIT KUMAR
SG	MS. SHILPI GARG
SC	MR. SUBHASH CHAND

AM-III	Applied Mathematics-III
EE-II	Electrical Engg. -II
ES	Environmental Studies
EDC	Electronics Device & Circuits
DE	Digital Electronics
UHV	Universal Human Values
EW	Electronics Workshop
SCA	Student Center Activity

  
 प्रधानाचार्य  
 राजकीय पॉलीटेक्निक  
 जानसठ (मुजफ्फरनगर)

  
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